		STUDY MODULE D	ES					
					Co			
		ctronic processing of sig	nais	Profile of study	10	10341761010329417 Year /Semester		
Field of Mat	hematics in Tech	nology		(general academic, practica general academic		3 / 6		
	e path/specialty			Subject offered in:		Course (compulsory, elective)		
		ircuits and measurement	1	Polish		obligatory		
Cycle o	of study:		For	m of study (full-time,part-time)			
First-cycle studies				full-time				
No. of	hours					No. of credits		
Lectu	re: 30 Classes	s: - Laboratory: 30		Project/seminars:	-	4		
Status of the course in the study program (Basic, major, other) (university-wide, from another field								
		other		univ	ers	ity-wide		
Education areas and fields of science and art						ECTS distribution (number and %)		
tech	nical sciences					4 100%		
	Technical scie	ences				4 100%		
Responsible for subject / lecturer: dr hab. inż. Andrzej Odon email: email: andrzej.odon@put.poznan.pl tel. 616652599 Elektryczny								
	Piotrowo 3A 60-965 Po	oznań I <mark>s of knowledge, skills an</mark> t	de	acial competencies				
TICI		_ :		-				
1	Knowledge	knowledge of electronic analog	d mathematical analysis, electrical engineering and basic circuits and digital techniques.					
2	Skills	Proper selection of electronic co simple electronic engineering tas	omponents and layout design for the implementation of a isk					
3	Social competencies He is aware of the need to broaden his / her competence and shows willingness to cooperate within the team and ability to meet the requirements of participation in the didactic process realized by the university.							
Assı	mptions and obj	ectives of the course:						
- Learn about the characteristics and application possibilities of analog, digital-to-analog and digital-analog converters.								
- Learn about modern measurement signal processing techniques								
	Study outco	mes and reference to the	edu	ucational results fo	r a f	field of study		
Knov	wledge:							
	ll-ordered knowledge o W25, K_W26]]	on the classification of basic electro	onic	components and methods	s of p	processing electrical signals.		
2. Car [[K_W		techniques of acquisition and pro	oces	sing of measurement sign	als f	or industrial applications -		
Skill	S:							
	n design and implement cal malfunction - [[K_	it signal processing for simple mea U23]]	asure	ement engineering applica	ations	s and diagnose the cause of		
evalua	ate non-technical aspec	ne and in team for the proper sele cts such as time and cost of install			sing	tasks and to properly		
Soci	al competencies:							
	she is able to think and eering - [[K_K03]]	d act in a responsible and entrepre	eneu	rial manner in the area of	elec	tronic signal processing		
		Assessment metho	ds d	of study outcomes				

Lectures:

- Assessment of the knowledge demonstrated on the test of the course content (test, accounting and problem questions), the awarding marks in laboratory exercises.
- continuous estimation in all classes (awarding attendance in lectures, activity and quality of perception).

Laboratory exercises:

- Assessing the knowledge needed to solve problems in the field of laboratory tasks,

- continuous estimation in all classes -,

- assessment of knowledge and skills related to the implementation of the measurement task, evaluation of the report of the exercise.

Course description

Updating 2017:

Methods of education are orientated to students to motivate them to participate actively in education process by discussion and reports.

Lectures:

Multimedia presentations (including drawings, photographs, videos) supplemented by examples on the board. At the end of the assessment, the activity of students during the classes is taken into account. Theoretical issues are presented in close connection with practice.

Laboratory:

Detailed review of the report by the teacher. Performing team work and performing experiments including:

- Application of operational amplifiers for the implementation of analogue signal transducers. Analogue Signal Converters (Voltage Converters, Voltage Converters, Voltage, Voltage Converters, Voltage, V

Basic bibliography:

1. Z. Kulka, A. Libura, M. Nadachowski, Przetworniki analogowo-cyfrowe i cyfrowo-analogowe, WKŁ, Warszawa 1987

2. U. Tietze, Ch. Schenk, Układy półprzewodnikowe, WNT, Warszawa 2009

3. J. Zakrzewski, Czujniki i przetworniki pomiarowe, Wyd. Politechniki Śląskiej, Gliwice 2004

Additional bibliography:

1. J. Jakubiec, J. Roj, Pomiarowe przetwarzanie próbkujące, wyd. Politechniki Śląskiej, Gliwice 2000

2. Denton J. Dailey, Electronic Devices and Circuits, copyright 2001 by Prentice-Hall, Inc., Upper Sadle River, New Jersey 07548, USA. Warszawa 2002.

Result of average student's workload

Activity	Time (working hours)						
1. Participation in lectures		30					
2. Participation in laboratory exercises	30						
3. Participation in consulting with the teachers	4						
4. Preparation to laboratory exercises and preparation of the reports	15						
5. Preparation to exam and participation in exam	12						
Student's workload							
Source of workload	hours	ECTS					
Total workload	91	4					
Contact hours	65	2					

45

2

Practical activities